

uPD17103

General purpose



| Operating conditions | |
|-----------------------|-----------|
| Voltage [V] | 2.7 - 6.0 |
| Dhrystone Mips | |
| Max. Clock [MHz] | |
| Min. Instr. Time [ns] | |
| Sub-Clock | |
| Tmin [°C] | |
| Tmax [°C] | |

| Memory | |
|-----------------------|------|
| ROM [KBytes] | 1 |
| Type | Mask |
| RAM [Bytes] | 8 |
| EEPROM [Bytes] | 0 |
| Instr. Cache [KBytes] | 0 |
| Data Cache [KBytes] | |

| Core | |
|---------------------------|-------|
| CPU | 17K |
| Instructions | |
| Internal Bus [Bits] | 4-bit |
| External Bus [Bits] | |
| Internal Interrupts [No.] | |
| External Interrupts [No.] | |

| Peripherals | |
|--------------------------|----|
| I/O Ports | 11 |
| LCD | NO |
| FIP | 0 |
| ADC | NO |
| DAC | NO |
| DMA Channels | 0 |
| Special Bus | NO |
| UART | 0 |
| Serial Clocked Interface | 0 |
| I2C | NO |
| Timer Channels | 0 |
| Watchdog | |
| PWM | NO |
| Other Peripherals | |

| Packages | |
|----------------|-----------------|
| Packages types | 16 DIP , 24 SOP |

| Feature | General purpose |
|---------|-----------------|
|---------|-----------------|

| Remark | |
|--------|--|
|--------|--|

Copyright NEC Electronics (Europe) GmbH 2001